#4

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

MATSUOKA, Hideyuki et al.

Intl. Appln. No.: PCT/JP00/03723

Intl. Filing Date: 8 June 2000

U.S. Appln. No.: 10/009,826

For: SEMICONDUCTOR MEMORY DEVICE AND MANUFACTURING METHOD

THEREOF

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

"

Pursuant to 37 C.F.R. § 1.56, and without any assertion as to materiality or prior art effect, the documents listed on the attached Form PTO-1449 are hereby cited.

Documents AA and AB on the attached list are cited in the specification, on page 3, and their relevance is indicated therein. Documents AC, AD, AL and AM were cited in the International Search Report (copy attached).

Respectfully submitted,

MWS:sjk

Miles & Stockbridge P.C. 1751 Pinnacle Drive Suite 500 McLean, Virginia 22102-3833 (703) 903-9000 March 19, 2002

Mitchell W. Shapiro

Reg. No. 31,568

FORM PTO-1449					Atty. Docket No.		App	Appln. No.	
LIST OF DOCUMENTS CITED BY APPLICANT					XA-9594		10	10/009,826	
					Applicant		1		
					MATSUOKA,	Hideyu	ki et	: al.	
					Filing Date Group		oup		
	*		U.S.	PATENT DO	CUMENTS	.			
Examiner	r	Document Number	Date		ame	Class	Sub-	Filing Date	
Initial							class	Filling Date	
	AA	5,745,407	04/28/98	Levy et a	ıı.	365	159		
	AB	5,535,156		Levy et a		365	175		
	AC	5,483,482		Yamada et		365	175		
	AD	5,665,978		Uenoyama		257	30		
	AE	5,684,737	11/04/97	Wang et a	il.	365	175		
	AF								
	AG								
	AH								
	ΑI				-				
	<u></u>		FOREIG	N PATENT I	OCUMENTS	<u> </u>			
Examiner Initial		Document Number	Date	Cou	ntry	Class	Sub- class	Translation	
	AL	10-125939	05/15/98	Japan				Мо	
	AM	11-17184	01/22/99	Japan				No	
	AN								
	AO								
	AP								
	AQ								
		OTHER	(including aut	hor, title, da	te, pertinent p	ages, etc.)		
	AR			· -					
•	AS								
	AT								
Examiner Date Considered									
	EP 6	Initial if ref 09; draw line y of this form	through ci	tation if r	not in confo	rmance a			